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	Subclass	ISSUE CLASSIFICATION
Class		

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.		PATENT DATE
MH SCANNED	7/14 Q.A.	04/11

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/779956	D F	365 438	258	2818 2813	Chen

APPLICANTS
Federico Pic

Electronic memory circuit and related manufacturing method

TITLE

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PTO-2040
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ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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